

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,444,318 B2
APPLICATION NO. : 10/719914
DATED : October 28, 2008
INVENTOR(S) : Sartaj Kumar Sahni and Haibin Lu

Page 1 of 2

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Cover page,

Column 1,

“(65) Prior Publication Data

US 2004/0258061 A1 Dec. 23, 2004

(51) Int. Cl.

G06F 7/00 (2006.01)

G06F 17/30 (2006.01)"

should read

--(65) Prior Publication Data

US 2004/0258061 A1 Dec. 23, 2004

Related U.S. Application Data

(63) Application No. 10/613,963, filed on Jul. 3, 2003, now U.S. Patent No. 7,509,300.

(60) Provisional application No. 60/393,445, filed Jul. 3, 2002.

(51) Int. Cl.

G06F 7/00 (2006.01)

G06F 17/30 (2006.01)--.

Column 3,

Line 30, “O(T)” should read --O(W)--.

Column 7,

Line 14, “less than s 3 fall” should read --less than s 3 fall--.

Column 8,

Line 34, “partition -150.” should read --partition -1 50--.

Column 9,

Line 39, “I 24,” should read --i ≥ 0,--.

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It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 14,

Line 8, "MULTIBITH" should read --MULTIBITb--.
Line 11, "that of MULTIBITH" should read --that of MULTIBITb--.
Line 12, "and in MULTIBITH" should read --and in MULTIBITb--.

Column 18,

Line 15, " $\sum_{j=0}^l s_j$ " should read -- $\sum_{j=0}^l s_j$, --.

Column 26,

Line 61, "O(logn+maxR)=O(g)" should read -- $O(\log n + \max R) = O(W)$ --.

Column 31,

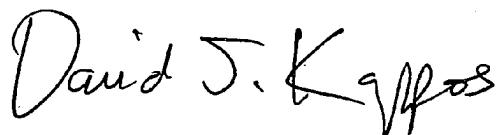
Line 20, "storing the prefix" should read --storing prefixes--.

Column 32,

Line 37, "value of the bits" should read --value of bits--.
Line 51, "value of the bits" should read --value of bits--.

Signed and Sealed this

Twenty-ninth Day of September, 2009



David J. Kappos
Director of the United States Patent and Trademark Office